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7590 12/03/2003

Finnegan, Henderson, Farabow,  
Garrett & Dunner, L.L.P.  
1300 I Street, N.W.  
Washington, DC 20005-3315



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(Depositor's name)
(Signature)
(D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/062,714	02/05/2002	Ming-Dou Ker	06720.0070	3110

TITLE OF INVENTION: SCR DEVICES IN SILICON-ON-INSULATOR CMOS PROCESS FOR ON-CHIP ESD PROTECTION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1330	\$300	\$1630	03/03/2004
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EXAMINER	ART UNIT	CLASS-SUBCLASS
MANDALA, VICTOR A	2826	257-347000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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1 FINNEGAN, HENDERSON,  
2 FARABOW, GARRETT &  
3 DUNNER, L.L.P.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

INDUSTRIAL TECHNOLOGY RESEARCH INSTITUTE

Taiwan, Republic of China

Please check the appropriate assignee category or categories (will not be printed on the patent);  individual  corporation or other private group entity  government

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(Authorized Signature) *Yitai Hu Reg. 24,034* (Date) *3/3/04*  
for Yitai Hu Reg. No. 40,653

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01 FC:1501	1330.00	OP
02 FC:1504	300.00	OP
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